

SYSTEM AND METHOD FOR REDUCING CHARGE
DAMAGE IN SILICON ON INSULATOR TECHNOLOGY

Inventor: James D. Gallia, et al

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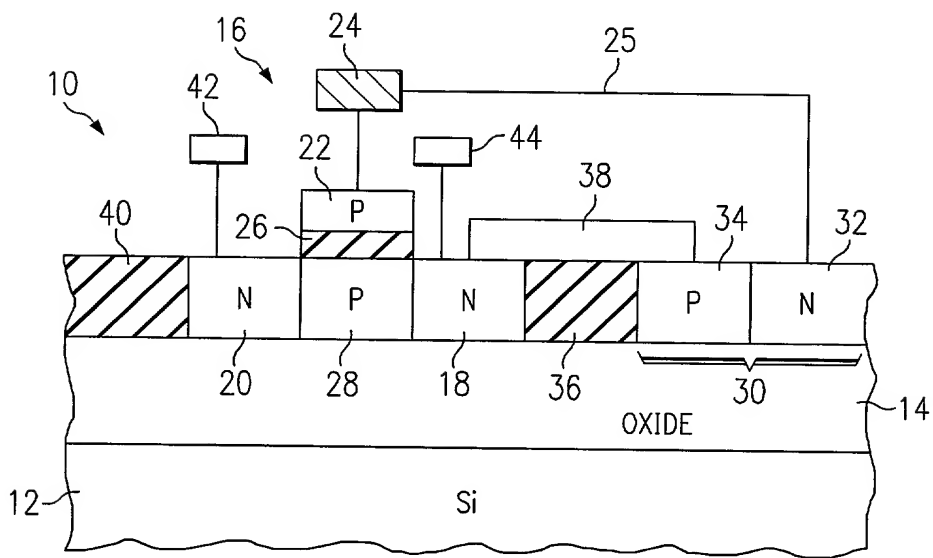


FIG. 1A

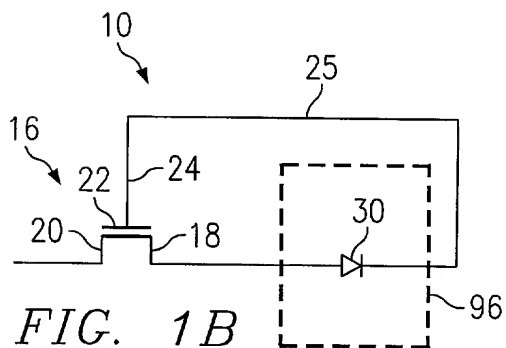


FIG. 1B

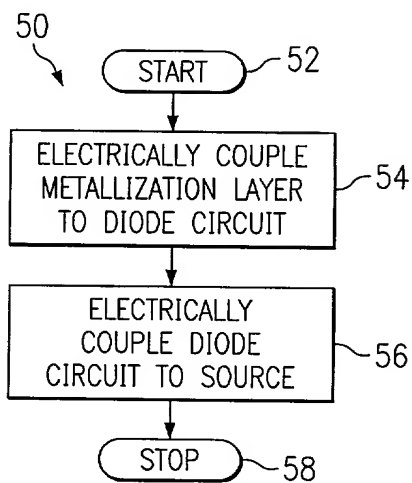


FIG. 1C

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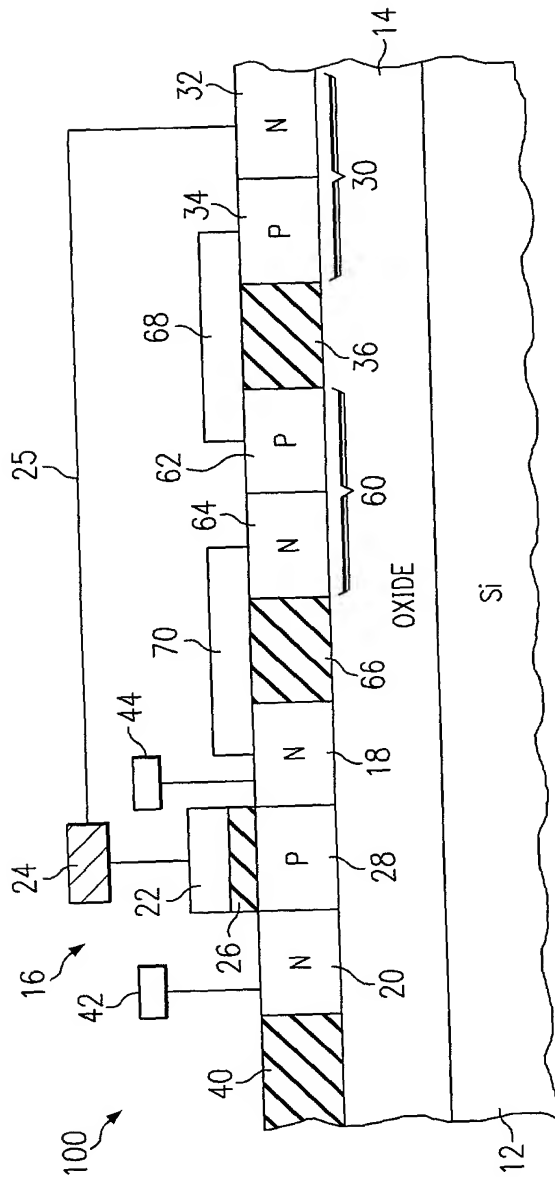


FIG. 2A

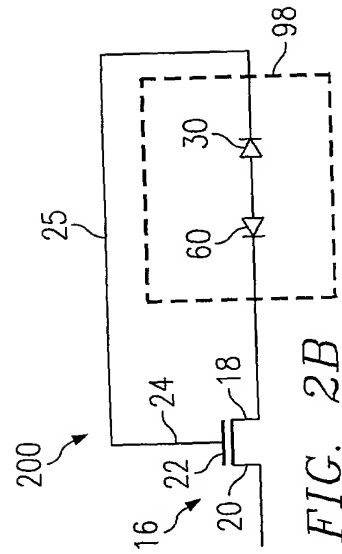


FIG. 2B

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